

IEEE-ELECTRON DEVICES SOCIETY DELHI CHAPTER

Presents

Workshop on

On

**TNL Atomistic TCAD Tools for New Era
Semiconductor Process & Device Technologies**

By

**Dr.Praveen Saxena
CEO & CTO,
Tech Next Lab Private Limited
Lucknow**

On

Saturday, 6th April, 2019

at

**Room No. 115, Arts Faculty, University of Delhi South Campus,
New Delhi**

Please note:

- Total number of seats for the workshop is 25 (on first come, first serve basis).
- There is no registration fee for IEEE EDS members. Non IEEE EDS members have to pay registration fee of INR 250.

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Technical Schedule

9.00AM-10:00AM	Registration
10:00AM-10:15AM	Inauguration and Welcome address by Professor Mridula Gupta, Chairperson IEEE EDS Delhi Chapter.
10:15AM-11:15AM SESSION-I	MBE & MOCVD Reactors Modeling for Epitaxial growth Through EpiGrow Simulator
11:15AM-11:30AM	Tea Break
11:30AM-1:00PM SESSION-II	Full Band Analysis and Material Characterization Through FullBand and ElecMob Simulators
1:00PM-2:00PM	Lunch
2:00PM-3:30PM SESSION-III	Monte Carlo Particle Device Simulation for FD SOI MOSFET Technology
3:30PM-3:45PM	Valedictory Session

Brief Bio of the speaker:

Dr. Praveen Kumar Saxena is serving as Chief Executive officer and Chief Technology officer of Tech Next Lab Pvt Ltd, Lucknow. He holds Ph.D. degrees in Electronics Engineering from IIT, Banaras Hindu University, India. He is working on Modeling & Designs of different IR technologies since 2005. His research area includes CMOS & Optical devices (photoconductive, photovoltaic, QWIP, SLS type I & II) and MEMS especially on cantilever based applications. He has several years of experience in academia & industry. He is recipient of several national & international awards. He worked closely associated with Indian Defence for development of IR detectors development. He was previously Regional Head for South East Asian territory at Silvaco Singapore.